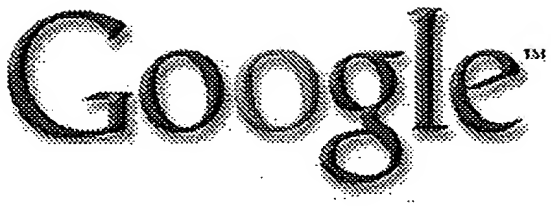

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("write back" or "store in" or "copy back") and MESI

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Prev Page 1 2 3 4 5 6 7 8 9 10 Next Page

1 Memory system performance of UNIX on CC-NUMA multiprocessors 95%

John Chapin , A. Herrod , Mendel Rosenblum , Anoop Gupta

ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1995 ACM SIGMETRICS joint international conference on Measurement and modeling of computer systems May 1995

Volume 23 Issue 1

This study characterizes the performance of a variant of UNIX SVR4 on a large shared-memory multiprocessor and analyzes the effects of possible OS and architectural changes. We use a nonintrusive cache miss monitor to trace the execution of an OS-intensive multiprogrammed workload on the Stanford DASH, a 32-CPU CC-NUMA multiprocessor (CC-NUMA multiprocessors have cache-coherent shared memory that is physically distributed across the machine). We find that our version of UNIX accounts for 24% of ...

2 Performance evaluation of a commercial cache-coherent shared memory multiprocessor 94%

Rajeev Jog , Philip L. Vitale , James R. Callister

ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1990 ACM SIGMETRICS conference on Measurement and modeling of computer systems April 1990

Volume 18 Issue 1

This paper describes an approximate Mean Value Analysis (MVA) model developed to project the performance of a small-scale shared-memory commercial symmetric